

# Curriculum Vitae

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Hardware design engineer

Arsenii Maksimov

born 26.08.1988

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## SUMMARY

Experienced electronics engineer with 5 years of practice, who aims to work in a progressive team, has good analytical skills and a passion for research, is ready to share experience with teammates and to learn new approaches to work.

Interested in schematics and PCB design for microcontroller-, DSP- and FPGA-based systems

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## PROFESSIONAL SKILLS

- 5+ years of digital and analog schematics design and PCB design
- Experienced with STM-microcontrollers, FPGA Xilinx Spartan, AD BlackFin schematics
- Physical interfaces: I2C, SPI, DDR, RS-232/422/485, USB, Ethernet 10/100
- 3+ years of experience of EMC-stable hardware design
- Familiar with EMC testing IEC 61000
- Familiar with Altium Designer and Mentor Graphics
- Experienced in simulations with CST Microwave Studio, NI Multisim, PSpice
- Good skills of hardware troubleshooting and SMD-soldering
- Basic knowledge of C/C++, NI Labview, Matlab/Simulink

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## WORK EXPERIENCE

*October 2016 – current time*

*Robert Bosch Car Multimedia, Hildesheim, Germany*

*Hardware project engineer*

**Tools:** VBA, SAP, Zuken CR5000

**Projects:** Infotainment systems for Nissan and Renault

- Schematic rework of Bluetooth/Wi-Fi modules according to customer orders
- Optimization of work process in the developers group
- Forming material orders and correction of bills of materials in SAP
- Producing assembly documentation

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*January 2016 – July 2016*

*Design Laboratory "Sintels", Moscow, Russia*

*Hardware design engineer*

**Tools:** Mentor Graphics, Matlab

**Project:** GSM-Basestation

- Design of schematic sheets and PCB for GSM base station with FPGA Xilinx Spartan and DSP BlackFin
  - Producing documentation
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<p><i>June 2015 – December 2015</i></p>	<p><i>Design Bureau “COBRA”, Vladimir, Russia</i>  <i>Hardware design engineer</i>  <b>Tools:</b> Altium Designer, NI Multisim  <b>Projects:</b> Jamming station for counterterrorist applications  <b>Responsibilities:</b></p> <ul style="list-style-type: none"> <li>● Design of schematic sheets and PCBs for STM under rough EMC-conditions</li> <li>● Design of digital interfaces: I2C, SPI, Ethernet 10/100</li> <li>● Prototyping and hardware troubleshooting with oscilloscopes, spectrum analyzers, signal generators, etc.</li> <li>● Producing documentation</li> </ul>
<p><i>July 2011 – May 2015</i></p>	<p><i>Design office “SYSTEMS AND TECHNOLOGIES”, Vladimir, Russia</i>  <i>Electronics engineer</i>  <b>Tools:</b> Altium Designer, CST Microwave Studio, Matlab, NI Multisim  <b>Projects:</b> Antenna development, EMC-protective devices for interfaces, Interface boards, EMC-certification tests, field tests  <b>Responsibilities:</b></p> <ul style="list-style-type: none"> <li>● Development of dual-band GPS/GLONASS PCB antenna</li> <li>● GPS-receivers schematics and PCB design</li> <li>● Design of surge protectors (IEC 61000-4-5) for RS-232/422/485 and USB interfaces</li> <li>● Handling EMC tests (IEC 61000)</li> <li>● Hardware prototyping and troubleshooting</li> <li>● Producing documentation</li> </ul>
<b>EDUCATION</b>	
<p><i>September 2005 – June 2011</i></p>	<p><i>Radio engineering, Vladimir State University, Russia.</i>  Master thesis: “Computer simulation of troposphere band channel”  Bachelor thesis: “Development of omnidirectional ultrawideband 1.8-5.5GHz micro-strip antenna”</p>
<b>INTERNSHIPS</b>	
<p><i>June – September 2009</i></p>	<p><i>Fraunhofer IIS, Entwicklungszentrum Röntgentechnik EZRT, Fürth.</i>  Topic: “Volumetric 3d image processing”  <b>Tasks and results:</b></p> <ul style="list-style-type: none"> <li>● Analyzed algorithms of cavity detection in metal samples</li> <li>● Implemented algorithms in C++ libraries and test cases</li> </ul>
<b>LANGUAGES</b>	
	<p>German – advanced B1  English – advanced B2  Russian – mother tongue</p>
<b>PERMISSION TO STAY</b>	
	<p>Blue Card EU, valid through 14.09.2020</p>
<b>INTERESTS</b>	
	<p>Saxophone  Long-distance running</p>